

Learning gem5 – Part II Modifying and Extending gem5

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A simple SimObject

http://learning.gem5.org/book/part2/helloobject.html



gem5's coding guidelines



Follow the style guide (<u>http://www.gem5.org/Coding Style</u>) Install the style guide when scons asks Don't ignore style errors

Use good development practices Historically mercurial queues Now: *git branches*



Adding a new SimObject



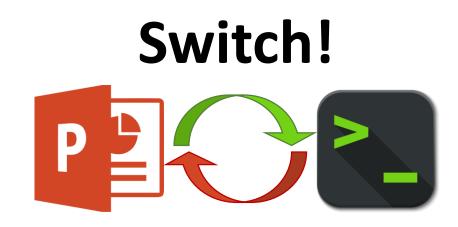
Step 1: Create a Python class

Step 2: Implement the C++

Step 3: Register the SimObject and C++ file

Step 4: (Re-)build gem5

Step 5: Create a config script





Step 1: Create a Python class



HelloObject.py from m5.params import * from m5.SimObject import SimObject class HelloObject(SimObject): type = 'HelloObject' cxx_header = 'learning_gem5/hello_object.hh' m5.params: Things like MemorySize, Int, etc.

type: The C++ class name

cxx_header: The filename for the C++ header file



Step 2: Implement the C++



hello_object.hh

#include "params/HelloObject.hh"
#include "sim/sim_object.hh"
class HelloObject : public SimObject

public:

HelloObject(HelloObjectParams *p);

params/*.hh generated automatically. Comes from Python SimObject definition

Constructor has one parameter, the generated params object.



};

Step 2: Implement the C++

hello_object.cc

HelloObjectParams: when you specify a Param in the Hello.py file, it will be a member of this object.

HelloObject::HelloObject(HelloObjectParams *params)

: SimObject(params)

std::cout << "Hello World! From a SimObject!" << std::endl;</pre>

HelloObject* <u>HelloObjectParams::create()</u>

return new HelloObject(this);

You must **define** this function (you'll get a linker error otherwise). This is how Python config creates the C++ object.





Step 3: Register the SimObject and C++ file

SConscript

Import(*)
SimObject('Hello.py')

Source('hello_object.cc')

Source(): Tell scons to compile this file (e.g., with g++).

Import: SConscript is just Python... but weird.

SimObject(): Says that this Python file contains a SimObject. Note: you can put pretty much any Python in here



Step 4: (Re-)build gem5



Step 5: Create a config script





Instantiate the new object that you created in the config file (e.g., simple.py)

> build/X86/gem5.opt configs/learning_gem5/hello.py

• • •

```
Hello world! From a SimObject!
```

• • •



Simple SimObject code

gem5

gem5/src/learning_gem5/part2/hello_object.cc
gem5/src/learning_gem5/part2/hello_object.hh
gem5/src/learning_gem5/part2/HelloObject.py
gem5/configs/learning_gem5/part2/hello_run.py





Debug support in gem5

http://learning.gem5.org/book/part2/debugging.html



Adding debug flags



Sconscript





Declare the flag: add the debug flag to the SConscript file in the current directory

DPRINTF(Hello, "Created the hello object");

DPRINTF: macr statements in g Found in "debug/hello.hh"

Debug string: Any C format string

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Debugging gem5



> build/X86/gem5.opt --debug-flags=Hello configs/tutorial/hello.py

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0: system.hello: Hello world! From a debug statement

debug-flags: Comma separated list of flags to enable. Other options include --debug-start=<tick>, --debug-ignore=<simobj name>, etc. See gem5.opt --help

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Event-driven programming

http://learning.gem5.org/book/part2/events.html



Simple event callback



class HelloObject : public SimObject
{
 private:

```
...
void processEvent();
EventFunctionWrapper event;
```

```
public:
   HelloObject(HelloObjectParams *p);
   void startup();
```

EventFunctionWrapper: Convenience class for simple events.

> **processEvent:** Callback function to run when event fires.

startup: Called after all
SimObjects instantiated.
Schedule local events here.

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};

Simple event callback



void HelloObject::processEvent() { timesLeft--; DPRINTF(Hello, "Hello world!" " Processing the event! %d left\n", timesLeft); if (timesLeft <= 0) { DPRINTF(Hello, "Done firing!\n"); } else { schedule(event, curTick() + latency); } }</pre>

schedule: Put an event instance on the event queue. An absolute tick used for when the event is processed.

curTick: Returns the current simulator time. Useful for relative time computations.

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Event SimObject code



http://learning.gem5.org/book/ downloads/hello object1.hh http://learning.gem5.org/book/ downloads/hello object2.cc





SimObject parameters

http://learning.gem5.org/book/part2/parameters.html



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Adding parameters



Param.<TYPE>: Specifies a parameter of type <TYPE> for the SimObject

Param.<TYPE>(): First parameter: default value. Second parameter: "help"

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Going further: More parameters



http://learning.gem5.org/book/part2/parameters.html

Included types (e.g., MemorySize, MemoryBandwidth, Latency)

Using a SimObject as a parameter

SimObject-SimObject interaction

src/learning_gem5/part2/hello_object.cc & hello_object.hh
src/learning_gem5/part2/goodbye_object.cc & goodbye_object.hh
src/learning_gem5/part2/HelloObject.py & GoodbyeObject.py

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Questions?

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We covered

How to build a SimObjectHow to schedule eventsDebug statements in gem5Adding parameters to SimObjects





MemObjects

http://learning.gem5.org/book/part2/memoryobject.html





Object that is part of gem5's memory system both classic caches and Ruby are MemObjects

Allowed to have MasterPorts and SlavePorts







Unit of transfer between MemObjects

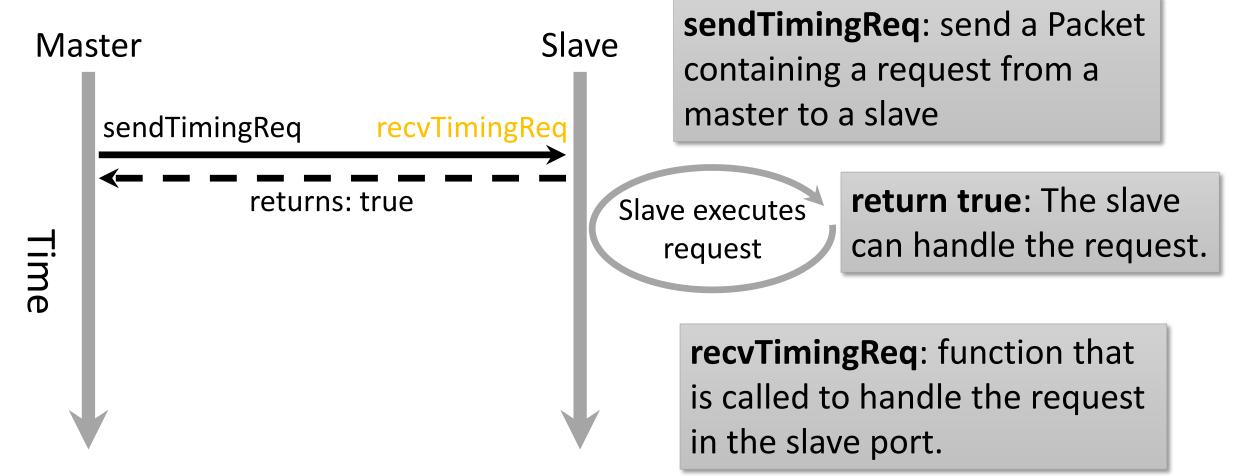
Packets pass between Master and Slave ports

Packets have Request Command Data Much more...



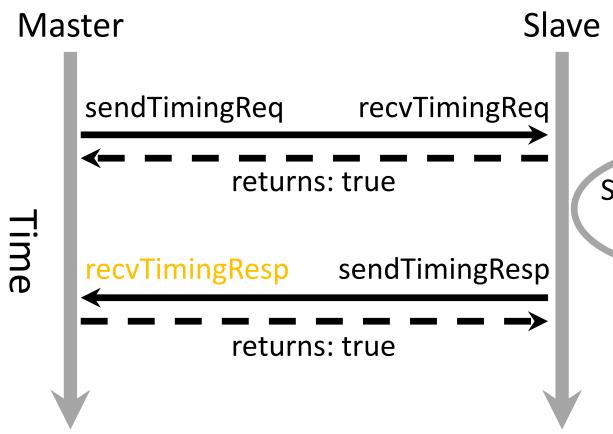










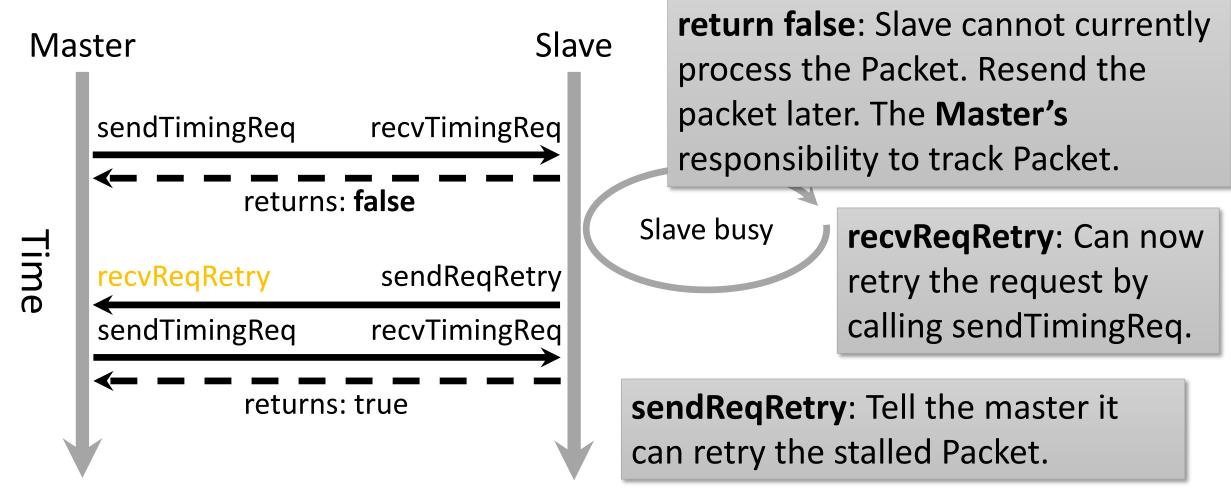


sendTimingResp: The slave finishes
processing the request, and now
sends a response (same packet).

Slave executes request

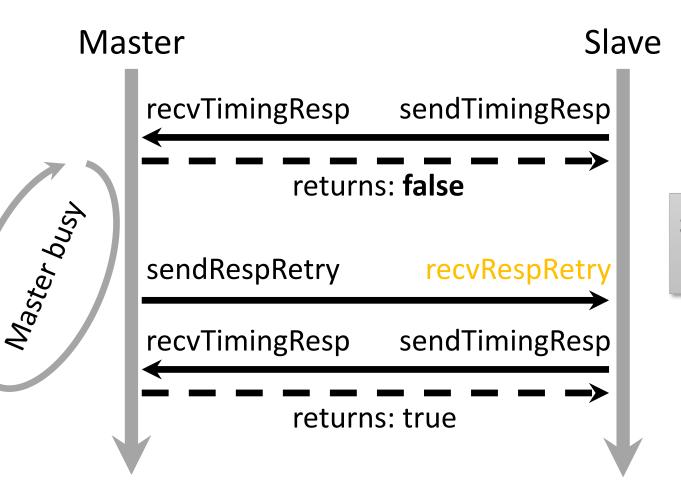
recvTimingResp: Handles the response from the slave. Returning true means the packet is handled.





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return false: Master cannot currently process the Packet. Resend the packet later. The **Slave's** responsibility to track Packet.

sendRespRetry: Slave can now retry the response.

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Master and slave port interface



Master

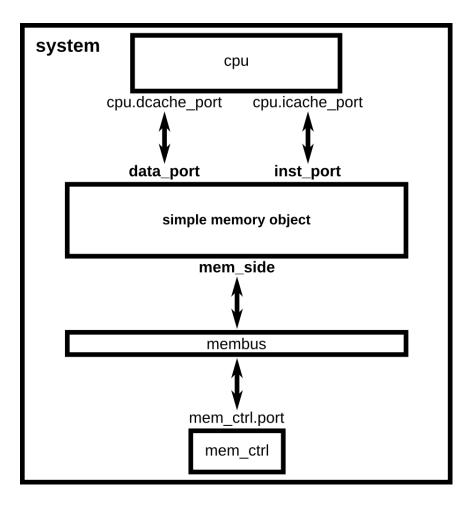
recv Timing Resp recv Req Retry recv Range Change

Slave

recv Timing Req recv Resp Retry recv Functional get Addr Ranges



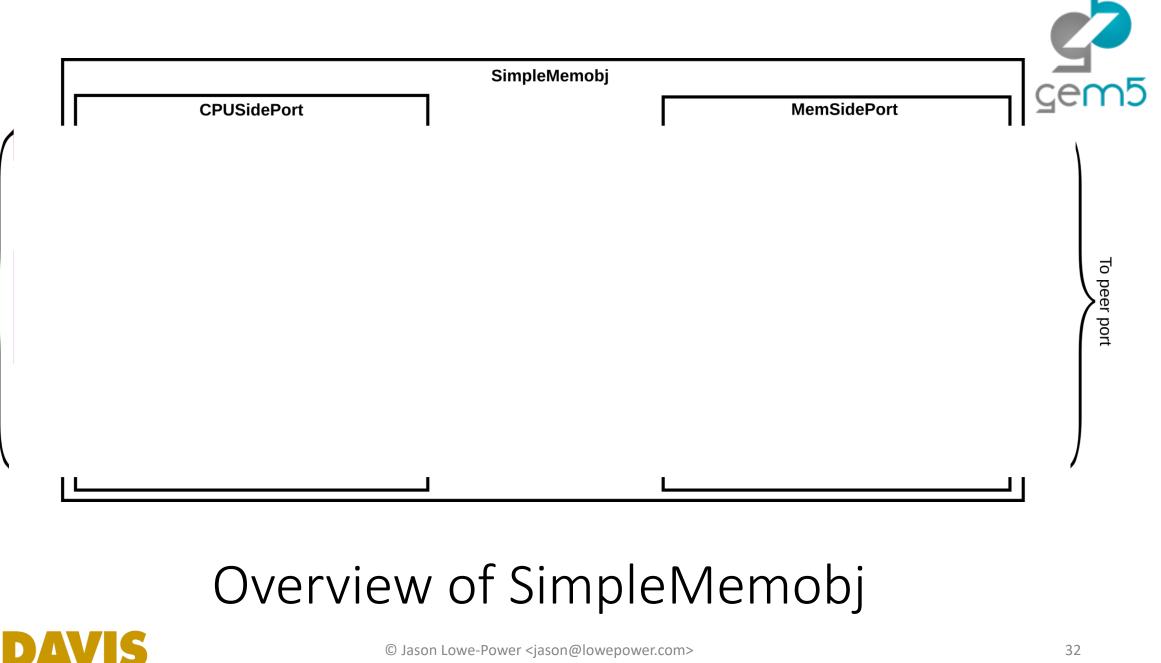
Simple MemObject



gem5

Switch!







SimpleCache

http://learning.gem5.org/book/part2/simplecache.html





Cache: A first "real" object

How to model...

Data storage

Tags std::map

Associativity

Data access latency Make an event

Blocking?

Could implement MSHRS...



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Handle request -> accessTiming with a delay AccessTiming ->accessFuntional to check for hit/miss ->if hit, reply

->if miss, upgrade request and send read

Handle response

Design

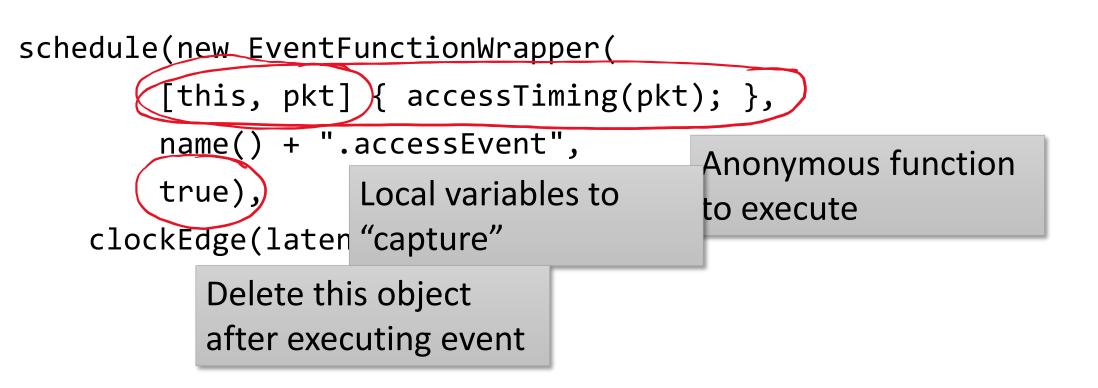
- -> insert new data (evict if needed)
 -> accessFunctional to read/write
 > read/write
- -> reply





More on events







Packet construction



Many different packet constructors See src/mem/packet.hh for details

Packet(Request, command)

Packet(Request, command, block size)
Make a packet that is block aligned (overrides request address)

createRead/createWrite(Request)
 Should probably use these convenience functions



Packets data allocation



Dynamic data: Will be deleted when the packet is deleted packet->allocate(): Allocates dynamic data

Static data: Give packet a pointer to the data. It will not delete it.

SenderState: Can be used to store "local" information





Packets: To delete or not to delete

Do **not** delete to send a response Call packet->makeResponse()

Do delete if you are the final sink for the packet E.g., a memory write

Do delete if you initiated the request and then received the response



Complete code available



Statistics

Better flow control

Code to make it work with O3CPU

Much more: http://learning.gem5.org/book/part2/simplecache.html



Questions?



We covered

How to make a MemObject

gem5 packets

The master – slave API in gem5

"Real" cache example

